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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	09/674,864	
	Filing Date	3/30/2001	
	First Named Inventor	Brightman, et al	
	Art Unit	2154	
	Examiner Name	El Hady, Nabil M.	
Total Number of Pages in This Submission	18	Attorney Docket Number	cportc01.007

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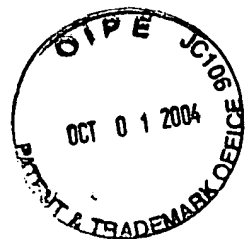
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Technology Center 2100

5    **Applicant:**                    Brightman, et al.                    **Paper No.:** 7

**Application No:**        09/674,864                    **Group Art Unit:** 2154

**Filed:**                    3/30/2001                    **Examiner:** El Hady, Nabil M.

10    **Title:** *Digital communications processor*

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15    Assistant Commissioner for Patents  
      Washington, DC 20231

**Response to a non-final Office action under 37 C.F.R. 1.111**

**Status of the prosecution**

20    The above patent application is the U.S. National Phase of PCT/US99/10002, filed 7 May 1999 and claiming priority from two US provisional patent applications, 60/084,706, filed 8 May 1998, and 60/105,823, filed 27 October 1998. On entry into the US National stage, the claims 1-13 of the PCT application were replaced by new claims 14-23. The PCT application received an International Preliminary Examination under PCT Chapter 2

25    in which claims corresponding to the present claims 16, 17, 20-23 were found to meet the requirements of PCT Article 33(2)-(4).

In his first Office action in the above application, mailed 6/28/04, Examiner objected to the application as lacking a disclosure, rejected claims 14-16 under 35 U.S.C. 102(e) as

30    anticipated by U.S. patent 5,794,060, Hansen, et al, *General purpose, multiple precision, parallel operation, programmable media processor*, having a priority date of 8/16/95 (henceforth "Hansen"), rejected claim 17 under 35 U.S.C. 103(a) as being unpatentable over the combination of Hansen with U.S. patent 5,535,406, Kolchinsky, *Virtual processor module including a reconfigurable programmable matrix*, issued 7/9/96

35    (henceforth "Kolchinsky"), rejected claim 18 under 35 U.S.C. 103(a) as being unpatentable over the combination of Hansen with U.S. patent 6,216,179, Murata, et al., *System for processing an I/O request using an old program while loading a new program*

- which upon completion is used to process the I/O request*, having a priority date of 5/8/95 (henceforth “Murata”), and rejected claims 19-21 under 35 U.S.C. 103(a) as being unpatentable over the combination of Hansen with U.S. patent 6,172,990, Deb, et al., *Media access control micro-risc stream processor and method for implementing the*
- 5 *same*, filed 11/12/97 (henceforth “Deb”). Claims 22 and 23 are multiply dependent from claims 14-21, and Examiner rejected claim 22 under 35 U.S.C. 103(a) as being unpatentable over the references used to reject claims 14-21 and claim 23 as being unpatentable under those references combined with Deb.
- 10 Applicants are providing Examiner with the Abstract of PCT/US99/10002, are amending claim 18 to provide a lacking antecedent, and are otherwise traversing the rejections.

**Please amend the Specification by adding the following Abstract:**